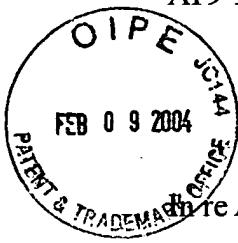


AT9-98-544

PATENT



- 1 -

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Pre Application of:
Balaram Sinharoy

Serial No.: 09/435,070

Filed: November 4, 1999

Title: CIRCUITS, SYSTEMS AND
METHODS DOING PERFORMING
BRANCH PREDICTIONS BY
SELECTIVELY ACCESSING BIMODAL
AND FETCH-BASED BRANCH HISTORY
TABLES

Before the Examiner:
William Wood

Group Art Unit: 2152

IBM Corp.
Intellectual Property Law
11400 Burnet Road
Austin, Texas 78758

#12/CN2
P.C. 12/04

RECEIVED

FEB 11 2004

Technology Center 2100

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

In response to the Office Action having a mailing date of November 5, 2003 (Paper No. 11), with a three-month shortened statutory period for response set to expire on February 5, 2004, please reconsider the rejection in view of the following remarks:

CERTIFICATION UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on February 5, 2004.


Signature

Barry S. Newberger
(Printed name of person certifying)

AT9-98-544

PATENT

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper; and

Remarks/Arguments begin on page 8 of this Paper.